

ABSTRACT OF THE DISCLOSURE

A write circuit arranged per a bit line or a plurality of bit lines includes a plurality of latch circuits for storing data written to a plurality of pages and bit line
5 connection circuits for connecting the plurality of latch circuits and bit lines and performs write operation to a plurality of pages by repeating continuous program operation which continuously performs program operations on a plurality of pages while a voltage generating circuit is
10 continuously generating a voltage necessary for program operation and continuous verify operation which continuously performs verify operations on a plurality of pages while the voltage generating circuit is continuously generating a voltage necessary for verify operation.